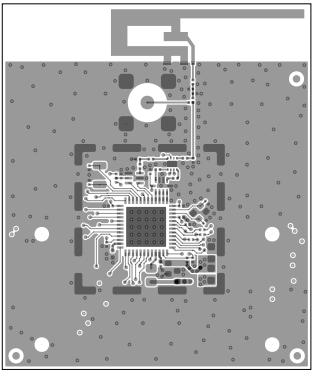



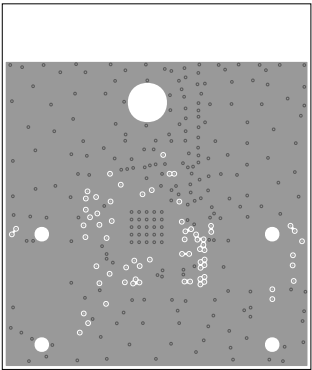



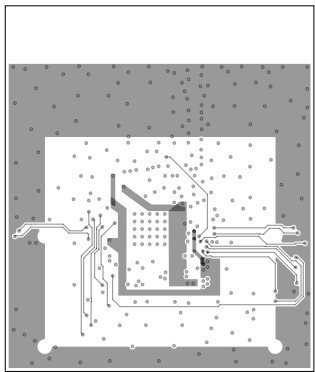

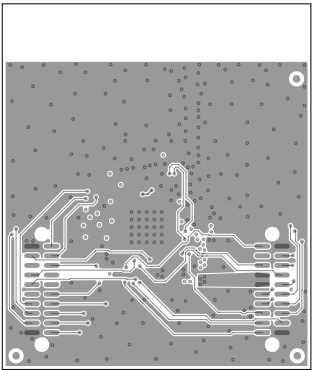



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PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um	<input type="checkbox"/> OTHER _____																																																																																																																																																	
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SILKSCREEN RESIST COLOR:	<input checked="" type="checkbox"/> GREEN	<input type="checkbox"/> OTHER _____																																																																																																																																																	
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SURFACE FINISH:	<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	<input type="checkbox"/> ENEPIG																																																																																																																																																	
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CERTIFICATION:	MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:																																																																																																																																																		
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DESIGNER: RGW		DATE: 2023-09-06																																																																																																																																																	
REVISION: E1		ALLEGRO DESIGNER VERSION: 17.4																																																																																																																																																	
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D	<div><div>Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div><div></div></div>					D																																																																																																																																													
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C	<div><div></div><div><div><div>DESIGN INFORMATION</div><div><div>MIN. TRACK WIDTH: MIN. CLEARANCE: MIN. VIA PAD SIZE:</div><div><div>0.15 mm 0.15 mm mm</div></div></div><div>MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/-150 um, HOLES +/-80 um HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/-80 um</div><div>MATERIAL:<div><div><input type="checkbox"/> FR-408</div><div><input type="checkbox"/> FR-4 High Tg</div><div><input type="checkbox"/> OTHER</div></div><div>THICKNESS: <input checked="" type="checkbox"/> 1.6mm +/-10%<div><input type="checkbox"/> OTHER</div></div><div>TOLERANCE:<div><input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</div><div><input type="checkbox"/> OTHER +/-</div></div><div>BOW & TWIST:<div><input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</div><div><input type="checkbox"/> OTHER +/-</div></div></div><div>DRILLING:<div>REFERENCE: <input checked="" type="checkbox"/> AS SHOWN<div><input checked="" type="checkbox"/> NC_DRILL FILES</div></div><div>PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um<div><input type="checkbox"/> OTHER</div></div></div><div>BOARD FINISH:<div>SILKSCREEN:<div><input checked="" type="checkbox"/> TOP<div><input checked="" type="checkbox"/> BOTTOM</div></div><div>SILKSCREEN COLOR:<div><input checked="" type="checkbox"/> WHITE<div><input type="checkbox"/> OTHER</div></div><div>SILKSCREEN RESIST COLOR:<div><input checked="" type="checkbox"/> GREEN<div><input type="checkbox"/> OTHER</div></div><div><input checked="" type="checkbox"/> MATTE<div><input type="checkbox"/> SEMI-GLOSS</div></div></div><div>SURFACE FINISH:<div><input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)<div><input type="checkbox"/> ENEPIG</div></div><div><input type="checkbox"/> IMM. TIN/SILVER OR EQUIV<div><input type="checkbox"/> OTHER</div></div></div><div>ARRAY/PANEL:<div><input type="checkbox"/> CUT AND TRIM PER MI BOARD OUTLINE</div><div><input type="checkbox"/> N.C. ROUTE<div><input checked="" type="checkbox"/> V. SCORE</div></div></div><div>CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:<div><div><input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS--><div><input type="checkbox"/> 1<div><input checked="" type="checkbox"/> 2<div><input type="checkbox"/> 3</div></div></div><div><input checked="" type="checkbox"/> RoHS<div><input type="checkbox"/> OTHER</div></div><div>PER ORDER</div></div></div><div>ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER</div><div>ADDITIONAL REQUIREMENTS:<div>MICROSECTION: <input type="checkbox"/> YES</div><div>BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE<div><input checked="" type="checkbox"/> REQUIRED<div><input type="checkbox"/> PER ORDER</div></div></div><div><input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE</div><div><input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE</div><div><input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE</div><div><input type="checkbox"/> TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE</div></div></div><div><div></div><div>TITLE:<div>CC1354P IPCs</div></div><div>PROJECT NUMBER:<div>CC1354P IPCs</div></div><div>FILE NAME:<div>filename.brd</div></div><div><div>DESIGNER:<div>RGW</div></div><div>DATE:<div>2023-09-06</div></div><div>REVISION:<div>E1</div></div></div><div><div>SCALE: 1.00</div><div>ALLEGRO DESIGNER VERSION:<div>17.4</div></div></div></div></div><div>Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div></div></div></div></div></div>						
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1	2	3	4	5	6	
A						A
B						B
C	<div><div></div><div><div>DESIGN INFORMATION</div><div>MIN. TRACK WIDTH: 0.15 mm MIN. CLEARANCE: 0.15 mm MIN. VIA PAD SIZE: mm MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/-150 um, HOLES +/-80 um HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/-80 um</div><div>MATERIAL: <div><div><input type="checkbox"/> FR-408</div><div><input type="checkbox"/> FR-4 High Tg</div><div><input type="checkbox"/> OTHER</div></div><div>THICKNESS: <input checked="" type="checkbox"/> 1.6mm +/-10%</div><div><div><input type="checkbox"/> OTHER</div><div></div></div><div>TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</div><div><div><input type="checkbox"/> OTHER +/-</div><div></div></div><div>BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</div><div><div><input type="checkbox"/> OTHER +/-</div><div></div></div><div>DRILLING: REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER</div><div>BOARD FINISH: SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER SILKSCREEN RESIST COLOR: <input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER <div><input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS</div><div>SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIG</div><div><input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER</div><div>ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER MI BOARD OUTLINE</div><div><input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE</div><div>CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: <div><div><input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS</div><div>-></div><div><input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3</div></div><div><input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER</div><div>ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER</div><div>ADDITIONAL REQUIREMENTS: MICROSECTION: <input type="checkbox"/> YES BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER <input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE <input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE <input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE <input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE <input type="checkbox"/> TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE</div></div><div><div> TEXAS INSTRUMENTS</div><div>TITLE: CC1354P IPCs</div><div>PROJECT NUMBER: CC1354P IPCs</div><div>FILE NAME: filename.brd</div><div>DESIGNER: RGW</div><div>DATE: 2023-09-06</div><div>REVISION: E1</div><div>ALLEGRO DESIGNER VERSION: 17.4</div><div>SCALE: 1.00</div></div></div><div>Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div></div></div></div>					C
D						D
1	2	3	4	5	6	

[illegible]

	1	2	3	4	5	6	
A							A
B							B
C	<div><div>19mm</div><div>29mm</div><div><div></div></div></div>						C
D	<div><div>Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div><div><div><div>CC1354P IPCs</div><div>CC1354P IPCs</div><div>filename.brd</div></div><div><div>DESIGNER:RGW</div><div>DATE:2023-09-06</div><div>REVISION:E1</div></div><div><div>SCALE: 1.00</div><div>ALLEGRO DESIGNER VERSION:17.4</div></div></div></div>						D
	1	2	3	4	5	6	

DESIGN INFORMATION		
MIN. TRACK WIDTH: 0.15 mm		
MIN. CLEARANCE: 0.15 mm		
MIN. VIA PAD SIZE: mm		
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C		
REGISTRATION TOLERANCES: METAL +/-150 um, HOLES +/-80 um		
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/-80 um		
MATERIAL:		
<div><div><input type="checkbox"/> FR-408</div><div><input type="checkbox"/> FR-4 High Tg</div><div><input type="checkbox"/> OTHER</div></div>		
THICKNESS: <input checked="" type="checkbox"/> 1.6mm +/-10% <input type="checkbox"/> OTHER		
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2		
<div><div><input type="checkbox"/> OTHER +/-</div></div>		
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2		
<div><div><input type="checkbox"/> OTHER +/-</div></div>		
DRILLING:		
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES		
PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER		
BOARD FINISH:		
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM		
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER		
SILKSCREEN RESIST COLOR: <input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER		
<div><div><input checked="" type="checkbox"/> MATTE</div><div><input type="checkbox"/> SEMI-GLOSS</div></div>		
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIG		
<div><div><input type="checkbox"/> IMM. TIN/SILVER OR EQUIV</div><div><input type="checkbox"/> OTHER</div></div>		
ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE		
<div><div><input type="checkbox"/> N.C. ROUTE</div><div><input checked="" type="checkbox"/> V. SCORE</div></div>		
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs		
TO MEET OR EXCEED THE REQUIREMENTS OF:		
<div><div><input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS</div><div>-></div><div><div><input type="checkbox"/> 1</div><div><input checked="" type="checkbox"/> 2</div><div><input type="checkbox"/> 3</div></div></div>		
<div><div><input checked="" type="checkbox"/> RoHS</div><div><input type="checkbox"/> OTHER</div></div> PER ORDER		
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.		
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER		
ADDITIONAL REQUIREMENTS:		
MICROSECTION: <input type="checkbox"/> YES		
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER		
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE		
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE		
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE		
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE		
TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE		
<div><div><div></div></div><div>TEXASINSTRUMENTS</div></div>		
TITLE: CC1354P IPCs		
PROJECT NUMBER: CC1354P IPCs		
FILE NAME: filename.brd		
DESIGNER: RGW		DATE: 2023-09-06
REVISION: E1		
SCALE: 1.00		ALLEGRO DESIGNER VERSION: 17.4

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